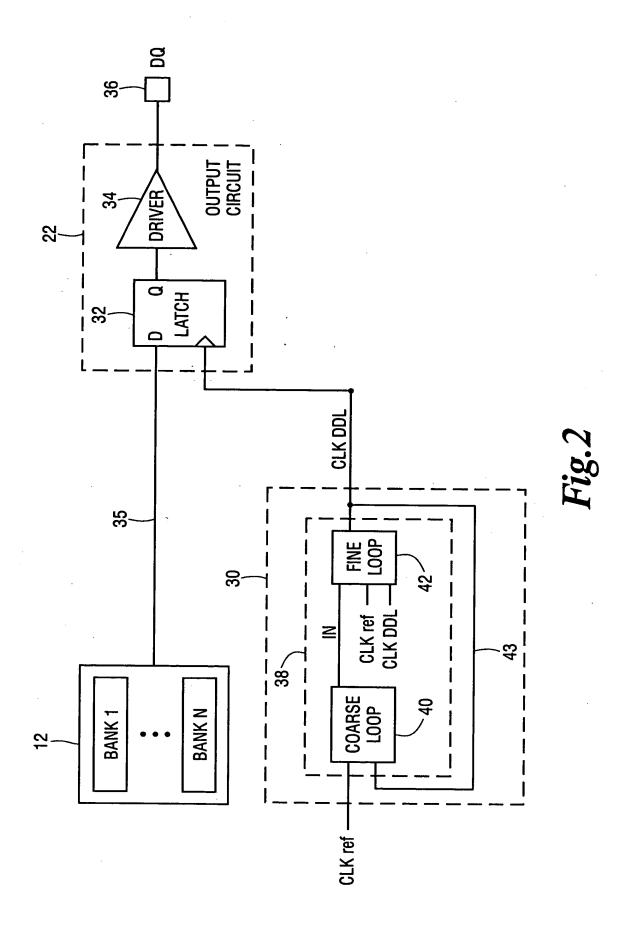
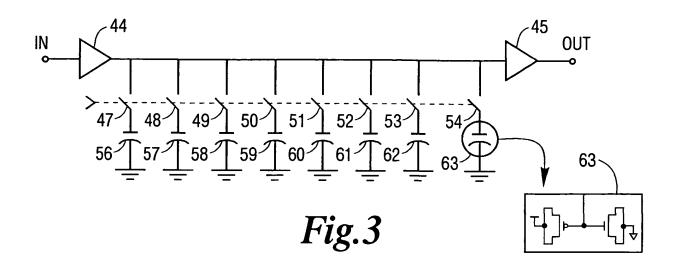


Fig. 1





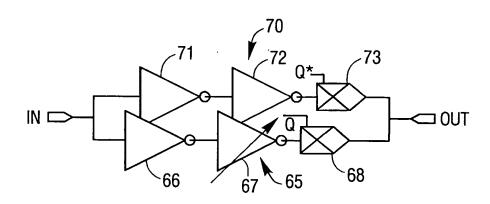


Fig.4

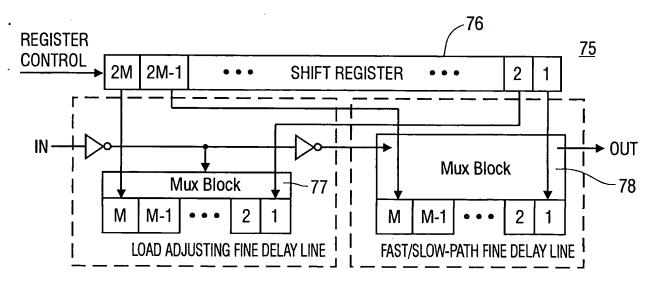


Fig.5

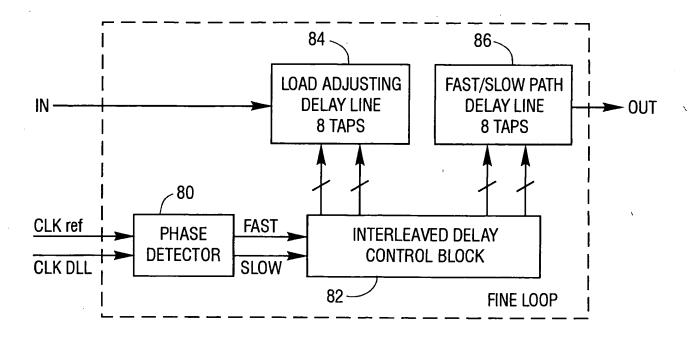
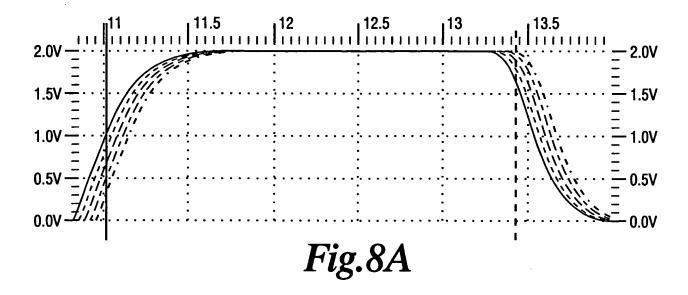
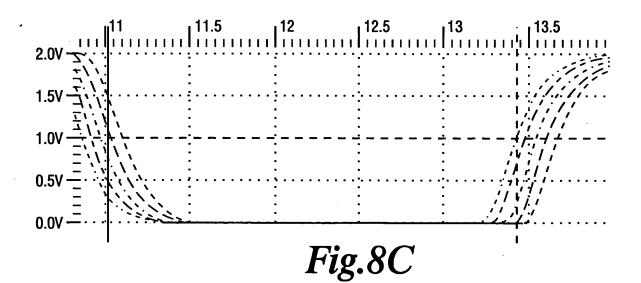


Fig.6 OUT IN \mathcal{E}_{n} 91 90 - 92 93 Fig.7A 80 CLK ref FAST PHASE SELECTION CONTROL BLOCK DETECTOR CLK DLL SLOW 88 Fig.7



2.0V = 1.5V = 1.5V = 1.5V = 1.0V = 1.



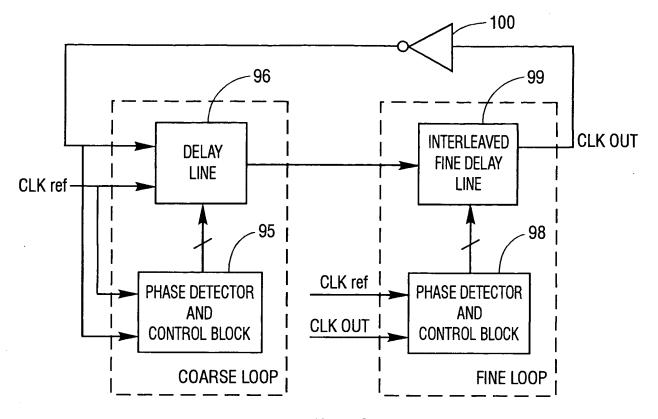


Fig.9

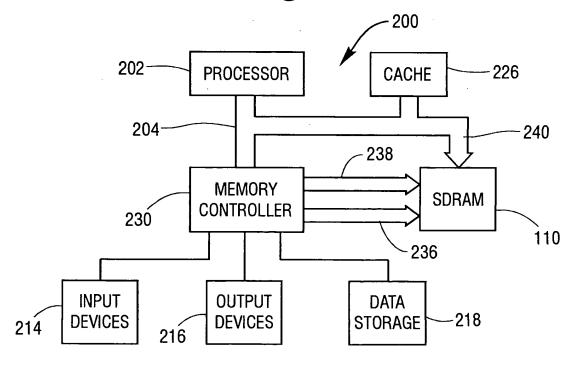


Fig.10